

ABSTRACT OF THE DISCLOSURE

A hard-macro arranged on a semiconductor chip ~~for~~  
~~constituting~~ constitutes a part of a semiconductor integrated  
5 circuit and includes at least one wire passing therethrough.  
The wire is formed in the hard-macro before the hard-macro is  
arranged on the semiconductor chip, and the wire starts at a  
first outer edge of the hard-macro and terminates at a second  
outer edge of the hard-macro intersecting with said first  
10 outer edge.